



hardware block + coverage + input signal + ex

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B Min, G Choi - Proceedings of the 2001 Pacific Rim International Symposium ..., 2001 - doi.ieeecomputersociety.org

... of conditions in real **hardware** de- signs ... variable whenever the basic **block** that the ... Generation Manipulating Condition Excitation **Coverage** Calculation Verilog ...[Cited by 2](#) - [Related Articles](#) - [Web Search](#)[RTL Functional Verification Using Excitation and Observation Coverage - group of 5 »](#)B Min, G Choi - [ieeexplore.ieee.org](#)... Key objective in RTL **hardware** verification is to check the correctness of a ... Figure 5. **Block** diagram of verification system ... The presented ECC **coverage** and en- ...[Related Articles](#) - [Web Search](#)[I'm done simulating; now what? Verification coverage analysis and correctness checking of the DECchip ... - group of 3 »](#)M Kantrowitz, LM Noack - Design Automation Conference Proceedings 1996, 33rd, 1996 - [ieeexplore.ieee.org](#)... For example, when testing prototype **hardware**, the self-checking focused tests ... Figure 5: Example of **Coverage** Case Analysis ... **block** size when the **block** size was 64 ...[Cited by 67](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)[\[PS\] Hardware Verification using PVS - group of 3 »](#)M Srivas, H Rueß, D Cyrluk - Formal Hardware Verification Methods and Systems in ..., 1997 - [csl.sri.com](#)... a primitive inference step that moves a **block** of conditionals ... relations AND2 and OR2 model two-**input** combinational conjunction ... **Hardware** Verification using PVS 11 ...[Cited by 15](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)[Detection of Response Time Failures of Real-Time Software - group of 6 »](#)

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WE Wong, T Sugela, Y Qi, JC Maldonado - The Journal of Systems & Software, 2005 - Elsevier

... of course assumes that the underlying **hardware** does not ... code in blocks (defined in